

### Alignment Procedure

#### 2.1 Preparation for Alignment

##### 2.1.1 Equipment and Tools Required

| Standard Test Equipment | Voltmeter                                    |
|-------------------------|----------------------------------------------|
|                         | Dual trace oscilloscope                      |
|                         | High voltage probe                           |
|                         | Hand tools as required                       |
|                         | Color analyzer, Minolta TV2150 or equivalent |
|                         | Computer with Acer 8215 card or equivalent   |

##### 2.1.2 Procedure of Preparation

1. Pre-set all VRs to the center position except R/G/B bias VR105, 106, 107 counter-clockwise to maximum.
2. Setup the unit and warm it up for at least 30 minutes.
3. The signal mode is 31/35KHz.

| Mode | Frequency (H/V) | Display            |
|------|-----------------|--------------------|
| 1    | 31.47K (+/-)    | 640 x 350 (VGA1)   |
| 2    | 31.47K (-/+)    | 640 x 400 (VGA1)   |
| 3    | 31.47K (-/-)    | 640 x 480 (VGA1)   |
| 4    | 35.51K (+/+)    | 1024 x 768 (8514A) |
| 5    | 35.15K (+/-)    | 800 x 600 (SVGA1)  |

#### 2.2 B+ Adjustment

1. Input mode 35KHz (SVGA1 800 x 600) with cross hatch pattern.
2. Set brightness and contrast keys to maximum position.
3. Adjust switch power supply VR601 to make video B+ 103 +/- 0.5 VDC.

#### 2.3 H-Hold Adjustment

1. Input mode 31KHz (VGA1 640 x 400) with full white pattern.
2. Short TP3 to the nearest ground.
3. Adjust VR253 (H-Hold) to make sure the picture almost stands up, then remove the short wire and make sure that the pitch is stable.

#### 2.4 Geometry Adjustment

1. Input mode 31KHz (VGA1 640 x 400) with full white pattern.
2. Set external contrast VR to maximum position.
3. Set external brightness VR to raster just cut off position.
4. Set external H-size, H-phase, V-size, V-center VRs to the center position.
5. Adjust VR256 (internal H-phase VR) to center the horizontal phase.
6. Adjust VR250 (pincushion VR) to meet pincushion specification.
7. Adjust VR302 (internal H-size VR) to meet horizontal size specification.
8. Adjust VR252 (internal V-size VR) to meet vertical size specification.
9. Check performances of all modes.

#### 2.5 Background Adjustment

1. Input mode 31KHz (VGA1 640 x 400) with background pattern.
2. Set external contrast and brightness VRs to maximum position.
3. Check the bias VRs of VR105, 106, 107 at counter-clockwise maximum position.
4. Adjust screen VR of FBT to obtain twilight raster about 1 to 1.5 Ft-L.
5. See which gun appears first, then adjust the two bias VRs of the other two non-appearing guns to achieve the color temperature meet specifications:  $x = 0.281 \pm 0.005$ ;  $y = 0.311 \pm 0.005$ .
6. Adjust screen VR of FBT again to let the raster about 1 to 1.5 Ft-L.

#### 2.6 Foreground Adjustment

1. Input mode 31KHz (VGA1 640 x 400) with 3" block pattern.
2. Set external contrast VR to maximum position.
3. Set external brightness VR to maximum position.
4. Check the drive VRs of VR103, 104, and sub-contrast VR306 at center position.
5. Adjust sub-contrast VR306 to let the light output become 70 Ft-L.
6. Set external brightness VR to minimum position.
7. Adjust contrast VR to let the light output become 15 Ft-L, then adjust VR103 to 104 to let the color temperature meet the specifications:  $x = 0.281 \pm 0.003$ ;  $y = 0.311 \pm 0.003$ .
8. Set contrast VR to maximum, then re-adjust VR306 to let the light output become 70 +/- 2 Ft-L.

## 2.7 ABL Adjustment

1. Input mode 31 KHz (VGA1 640 x 400) with full white pattern.
2. Set external contrast and brightness VRs to maximum position.
3. Check the ABL VR305 at the center position.
4. Adjust ABL VR305 to let the light output become  $30 \pm 2$  Ft-L.

## 2.8 Power Saving Function Check

1. Input mode 31 KHz (VGA1 640 x 400) with full white pattern.
2. Set external contrast and brightness VRs to maximum position.
3. Remove H-Sync, the video and raster should be extinguished, the power consumption is around 55W.
4. Remove V-Sync, the power consumption should be less than 5W and LED flash.
5. Input H-Sync and V-Sync, the video should be exhibited again and LED is on.

## 2.9 Focus Adjustment

1. Input mode 35KHz (8514A 1024 x 768) with "Reverse Character Pattern" (Windows Application).
2. Set external contrast VR to maximum position.
3. Set external brightness VR to raster just cut off position.
4. Adjust focus VR of FBT to make the video area focus clearer.

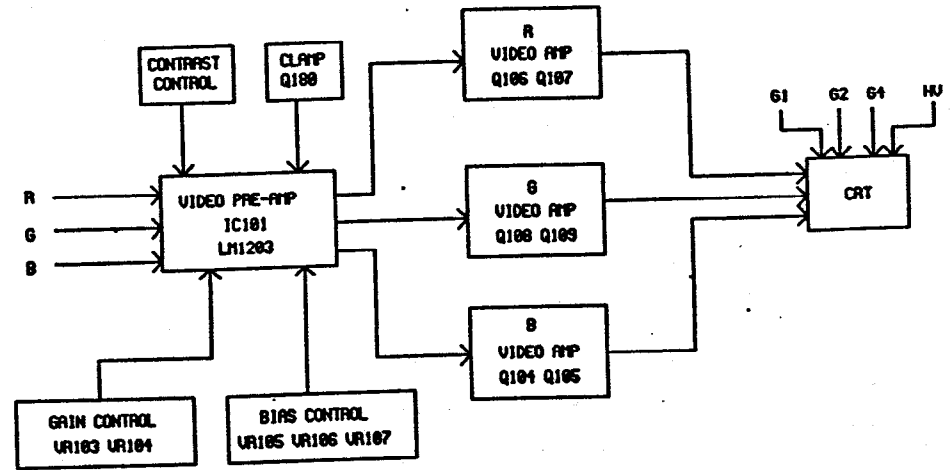
## 2.10 Convergence Adjustment

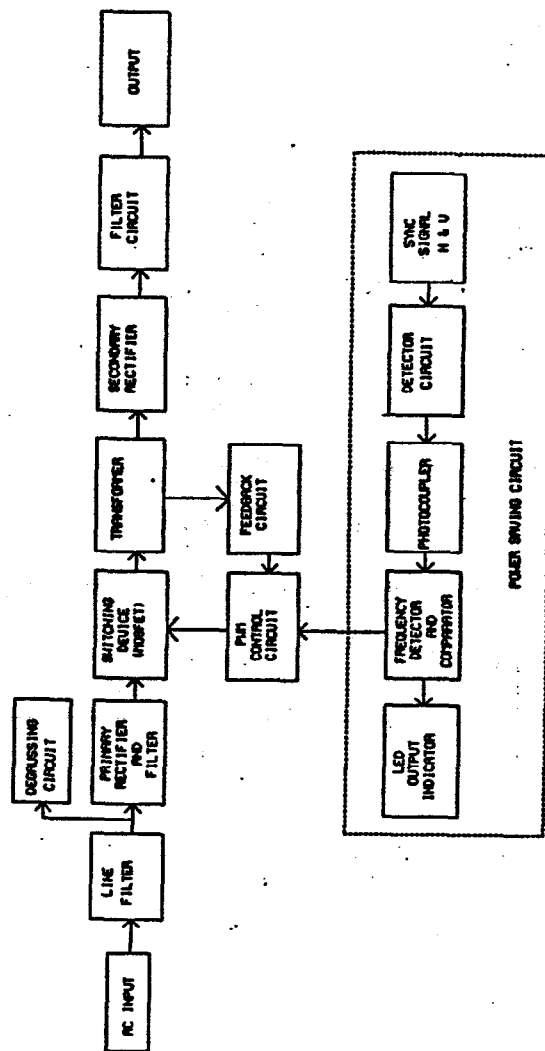
1. Input mode 31KHz (VGA1 640 x 400) with cross hatch pattern.
2. Adjust 4-pole and 6-pole of Yoke to meet the specification.

## 2.11 Geometry Specification

| Item | Description       | Specification  |
|------|-------------------|----------------|
| 1    | Hori Size         | $250 \pm 4$ mm |
| 2    | Vert Size         | $187 \pm 4$ mm |
| 3    | Side Pin          | $\leq 1.5$ mm  |
| 4    | Top/Bottom Pin    | $\leq 2.0$ mm  |
| 5    | Side Barrel       | $\leq 1.0$ mm  |
| 6    | Top/Bottom Barrel | $\leq 1.0$ mm  |
| 7    | Trapezoid         | $\leq 3.0$ mm  |
| 8    | Video Offset      | $\leq 4.0$ mm  |
| 9    | Parallelogram     | $\leq 3.5$ mm  |

### Deflection CKT Block Diagram





### 3.2 Mode Control Circuit

1. IC201 WT8043 is used for mode control. We can use it to detect the following timing:

| Mode | Frequency (H/V) | Resolution           |
|------|-----------------|----------------------|
| 1    | 31.47K (+/-)    | 640 x 350 (IBM VGA)  |
| 2    | 31.47K (-/+)    | 640 x 400 (IBM VGA)  |
| 3    | 31.47K (-/-)    | 640 x 480 (IBM VGA)  |
| 4    | 35.51K (+/+)    | 1024 x 768 (8514A)   |
| 5    | 35.15K (+/+)    | 800 x 600 (SVGA1)    |
| 6    | 37.86K (+/-)    | 640 x 350 (VESA VGA) |
| 7    | 37.86K (-/+)    | 640 x 400 (VESA VGA) |
| 8    | 37.86K (-/-)    | 640 x 480 (VESA VGA) |
| 9    | 37.879K (+/+)   | 800 x 600 (SVGA2)    |

2. Pinout of WT8043

| Pin No | Description                                                           |
|--------|-----------------------------------------------------------------------|
| 1      | Oscillation in                                                        |
| 2      | Oscillation out                                                       |
| 3      | Horizontal Sync Input                                                 |
| 4      | Vertical Sync Input                                                   |
| 5      | Horizontal Sync Output                                                |
| 6      | Vertical Sync Output                                                  |
| 7      | F45K - active low when the horizontal frequency is greater than 45KHz |
| 8      | F36K - active low when the horizontal frequency is greater than 36KHz |
| 9      | Ground                                                                |
| 10     | F33K - active low when the horizontal frequency is greater than 33KHz |
| 11     | Active low in 31KHz and 37KHz 640 x 350                               |
| 12     | Active low in 31KHz and 37KHz 640 x 400                               |
| 13     | Active low in 31KHz and 37KHz 640 x 480                               |
| 14     | Active low in 35KHz 800 x 600                                         |
| 15     | Active low in 37.5KHz 800 x 600                                       |
| 16     | Active low in 48KHz 800 x 600                                         |
| 17     | Active low in 35KHz 1024 x 768 interface (8514A)                      |
| 18     | Active low in 48KHz 1024 x 768 non-interface                          |
| 19     | No connection                                                         |
| 20     | VCC +5VDC                                                             |

### 3.4 Vertical Output Circuit

1. IC250 TDA4866 is used for vertical output amplifier.
2. The IC250 TDA4866 Pin Assignment:

| Pin Number | Description                    |
|------------|--------------------------------|
| 1          | Vertical oscillation input 1   |
| 2          | Vertical oscillation input 2   |
| 3          | VCC +12VDC                     |
| 4          | Vertical output V-             |
| 5          | Ground                         |
| 6          | Vertical output V+             |
| 7          | Flyback supply voltage + 40VDC |
| 8          | Vertical blanking pulse        |
| 9          | Feedback input                 |

3. Q250 sends the vertical blanking pulse to G1.
4. VR254 is used for vertical center control.

### 3.5 H.V. Shut Down Control

Q252 and Q251 are used for HV shutdown circuit. When HV is greater than 27KV, the voltage of FBT pin 9 (+40V) will turn on ZD250. Q251 and Q252 are also turned on. IC251 TDA4852 pin 2 gets a high level voltage to terminate the horizontal oscillator.

### 3.6 Horizontal Output Circuit

1. Q315 and T301 are used for horizontal driver.
2. The "Diode Modulation Structure" is used for the horizontal output circuit.
3.
  - Q310 is horizontal output transistor.
  - L302 is Linear coil.
  - C312 is Cs capacitor.
  - D307 and D308 are damper diodes.
  - C314 and C315 are tuning capacitors.
  - L303 is the modulating coil.
4. Q320 and Q301 are used as amplifiers for parabolic waveform. Q302 and Q303 merge the parabolic waveform of pin cushion and DC level of horizontal size control. It is used for "Diode Modulation" control.
5. B+ is 90 VDC in 31KHz, 103VDC in 35KHz, 113 VDC in 37KHz.

### 3.7 Spot Killer and Blanking Circuit

1. Spot Killer - when power is turned off, the residual electronic beam from the CRT burns the CRT phosphor. So it is necessary to make G1 more negative when power is turned off to stop the emission of the CRT electronic beam.
2. When power is on, +6.3VDC turns on Q332. G1 voltage is around - 30VDC. When power is off, +6.3VDC goes to 0VDC. It turns off Q332. G1 voltage goes to - 180VDC to stop beam emission.
3. Blanking - vertical blanking signal comes from pin 8 of IC250 TDA4866. Q250 makes the signal go reverse and higher than coupling to G1. There will be no vertical retrace lines visible.

### 3. Horizontal Phase Preset

- R202 is for 35KHz 8514A horizontal phase preset.
- R204 is for 37.5KHz 800 x 600 horizontal phase preset.
- R205 is for 35KHz 800 x 600 horizontal phase preset.
- R206 is for 31KHz IBM VGA horizontal phase preset.
- R207 is for 37KHz VGA horizontal phase preset.

### 4. Vertical Size Preset

- R214 is for 31KHz 640 x 480 IBM VGA vertical size preset.
- R215 is for 31KHz 640 x 400 IBM VGA vertical size preset.
- R216 is for 31KHz 640 x 350 IBM VGA vertical size preset.
- R217 is for 37KHz 640 x 480 VESA VGA vertical size preset.
- R218 is for 37KHz 640 x 400 VESA VGA vertical size preset.
- R219 is for 37KHz 640 x 350 VESA VGA vertical size preset.

### 5. Horizontal Size Preset

- R228 is for 35KHz 8514A horizontal size preset.
- R240 is for 37.5KHz 800 x 600 horizontal size preset.
- R241 is for 35KHz 800 x 600 horizontal size preset.
- R242 is for 31KHz IBM VGA horizontal size preset.
- R2415 is for 37KHz VESA VGA horizontal phase preset.

6. Since WT8043 can not distinguish 31KHz and 37KHz VGA, use Q205, Q202, Q201 and Q208 to distinguish 31KHz IBM VGA and 37KHz VESA VGA.

7. Q204, Q206, Q207 sends control signals F45A, F36A, F33A to control the switching power supply. This way, the switching power supply can send the correct horizontal B+ to FBT.

8. From R220 to R227 and R230 to R232 are used for pull-up resistors.

### 9. Truth Table for WT8043

| Hs Frequency       | F33K | F36K | F45K |
|--------------------|------|------|------|
| Hs < 33KHz         | 1    | 1    | 1    |
| 33KHz < Hs < 38KHz | 0    | 1    | 1    |
| 38KHz < Hs < 45KHz | 0    | 0    | 1    |
| 45KHz < Hs         | 0    | 0    | 0    |

## 3.3 Horizontal and Vertical Oscillation Circuit

1. IC251 TDA4852 merges the horizontal oscillation stage and vertical oscillation stage.
2. The IC251 TDA4852 Pin Assignment:

| Pin Number | Description                                            |
|------------|--------------------------------------------------------|
| 1          | VCC +12VDC                                             |
| 2          | AFC and HV shutdown                                    |
| 3          | Horizontal oscillation output                          |
| 4          | Ground                                                 |
| 5          | Vertical oscillation output 1                          |
| 6          | Vertical oscillation output 2                          |
| 7          | Ground                                                 |
| 8          | No connection                                          |
| 9          | Horizontal sync input from M301                        |
| 10         | Vertical sync input from IC201 pin 6                   |
| 11         | Parabolic waveform output for pin cushion compensation |
| 12         | Capacitor for amplitude control                        |
| 13         | Vertical size control                                  |
| 14         | Pin cushion control                                    |
| 15         | Vertical oscillator resistor                           |
| 16         | Vertical oscillator capacitor                          |
| 17         | PLL1 phase                                             |
| 18         | Horizontal hold control                                |
| 19         | Horizontal oscillator capacitor                        |
| 20         | PLL2 phase, Horizontal phase control                   |

3. Q253 reduces the horizontal jitter.

### 4. Horizontal Hold Control:

- VR253 is for 31KHz horizontal hold preset.
- R266 is for 35KHz horizontal hold preset.
- R260 is for 37KHz horizontal hold preset.

### 5. Pin cushion Control

- VR250 is for 31KHz pin cushion preset.
- R267 is for 35KHz pin cushion preset.
- R261 is for 37KHz pin cushion preset.
- R298 is for 31KHz and 37KHz 640 x 350 pin cushion preset.

6. R255 makes the vertical linearity correct.

7. R252 can make the compensation for pin cushion when vertical size is adjusted.

### 3.12 Switching Power Supply

The main purpose of the EMI filter is to reduce the conducted noise. There are two major aspects of conducted interference to be considered: the differential mode conducted noise and the common mode conducted noise.

The EMI filter consists of L601, C602, L602, C603, C604 and L603.

The input rectifier/filter converts the 47 - 63 Hz AC line voltage to a DC voltage.

The input rectifiers are configured as a full wave bridge.

Input CKT is universal such that input voltage is from 90Vac to 264Vac and input frequency is from 47Hz to 63Hz. When the input voltage equals 90Vac, input current equals 0.9A or input voltage equals 264Vac, input current equals 0.5A.

The switching device of power supply is MOSFET 2sk793. The maximum rating is  $I_{ds} = 5A$ .  $V_{ds} = 850V$  and PWM current mode controller is UC3842. The pin functions are as follows:

| Pin Number | Function                 | Pin Number | Function  |
|------------|--------------------------|------------|-----------|
| 1          | Error amplifier output   | 5          | GND       |
| 2          | Negative feedback        | 6          | Output    |
| 3          | Current sensor           | 7          | Vcc       |
| 4          | Sawtooth ramp oscillator | 8          | Vref = 5V |

The 711D/7133D power supply output is as follows:

6.3V 0.7A  
 12V: 0.7A  
 90V: 0.55A (Fh = 31.4KHz)  
 113V: 0.4A (Fh = 37KHz)

The power supply adds a snubber CKT to reduce the spike of MOSFET  $V_{ds}$  which results from the leakage of the transformer.

The snubber CKT consists of R605, C606, C607, D606.

The pin 6 output of UC3842 drives the gate of power MOS 2SK793, so the transformer primary winding comes into oscillatory square wave so as to store and transfer energy.

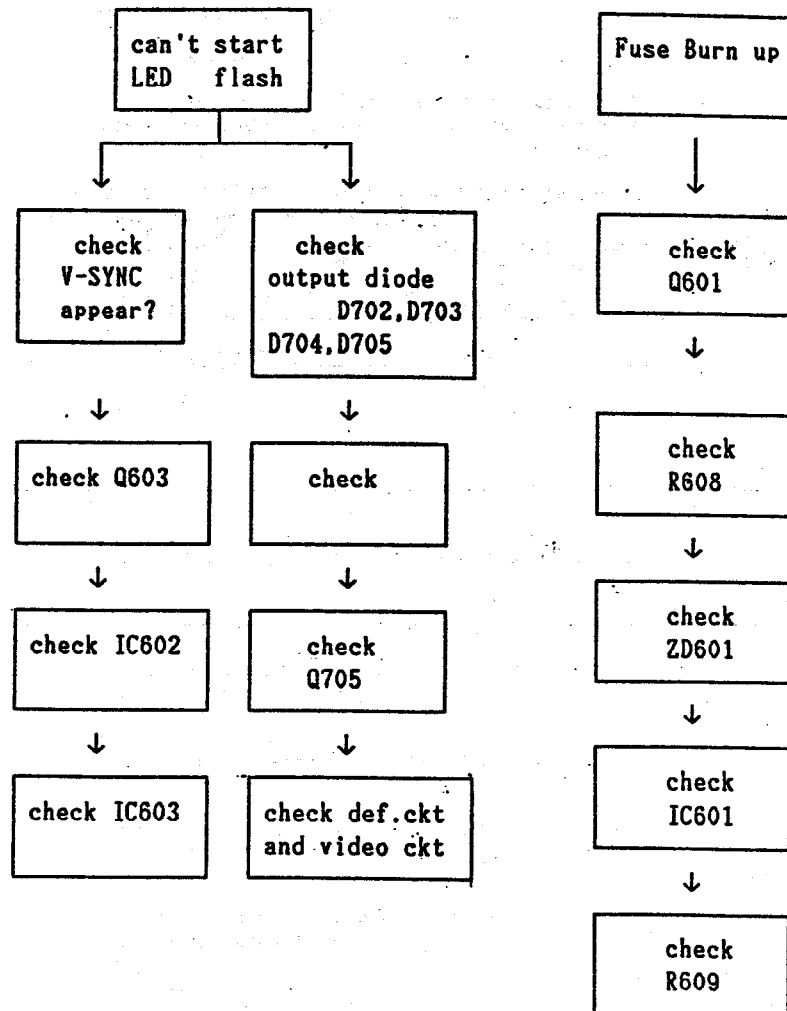
When MOSFET 2SK793 turns off, the secondary output diodes turn on and the secondary winding flux energy transfer to capacitor to supply load. Secondary connects the L.C. low pass filter in order to reduce ripple and noise.

There is power saving function in the switching power supply. When V-Sync appears, it is detected by an optocoupler through LM393 comparator, then UC3842 during normal operation. The monitor works at ON mode.

When V-Sync disappears, or V-Sync frequency is less than 10Hz, the UC3842 shuts down and the power supply does not output, then the monitor works at suspend mode (off mode) and the power losses are less than 5W to achieve power saving function.

### 3.13 Troubleshooting

#### 3.13.1 SPS Troubleshooting



### 3.8 Power Saving

1. When the user does not make use the PC, the PC makes the monitor go into "Power Saving Mode". The PC must have power management program.
2. There are four Power Saving Modes:

| Mode         | Description                                                                                                                                                                                                                                                                                                                       |
|--------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Standby Mode | PC removes the horizontal sync. In this mode, Q331 is turned off. It turns off Q332 at the same time. G1 goes to -180VDC. There is no video and raster shown on the screen. The monitor power consumption is about 55W.                                                                                                           |
| Suspend Mode | PC removes the vertical sync. In this mode, switching power supply is shut down. The power consumption of the monitor is less than 5W.                                                                                                                                                                                            |
| Off Mode     | PC removes the horizontal and vertical sync. In this mode, the switching power supply is shut down. The monitor power consumption is less than 5W.                                                                                                                                                                                |
| Burn-in Mode | When the signal connector is connected to the PC, the "EPS1" is shorted to the ground on the PC side. Q330 is turned off. When the signal connector is disconnected from the PC, the "EPS1" is floated. It turns on the Q330. In this mode, Q332 is always on, even without horizontal sync. We can see the raster on the screen. |

NOTE: There is no CRT heater pre-heat in suspend mode. The suspend mode and off mode are combined.

### 3.9 ABL (Auto Brightness Limit) Circuit

1. When the brightness is increased, the CRT beam current goes high. The beam current should not exceed 350 uA. It is necessary to limit the beam current to keep the CRT life longer.
2. When the brightness is increased, the CRT beam current goes high. The R350 and VR305 currents go high too. It creates more voltage drops on the resistors so the Q317 base voltage goes lower. It turns on Q317. The Q317 emitter is connected to the video-contrast control. At this time, the contrast voltage is pulled down. The beam current is limited.
3. To change the ABL working point, it needs to adjust VR305. With full white pattern, it is recommended to adjust it to 30F1-L.

### 3.10 Video Amplifier Circuit

1. The process circuit of R, G, and B are the same. Only the B circuit is discussed.
2. VR103 is used for video gain control. VR107 is used to cut off voltage control.
3. The cascade amplifier circuit is composed of Q104 and Q105.

### 3.11 BOM Difference Table

1. With Different CRT

| Location         | Panasonic LR CRT .28 CRT (7133D) | Panasonic Normal CRT .28 CRT (7133D) | Chunghwa LR CRT .28 CRT (7133D) | Chunghwa Normal CRT .28 CRT (7133D) | Chunghwa Normal .39 CRT (7111D) |
|------------------|----------------------------------|--------------------------------------|---------------------------------|-------------------------------------|---------------------------------|
| C330             | 22uF/160V                        | Omit                                 | 22uF/160V                       | Omit                                | Omit                            |
| D315             | 15D/4                            | Jumper                               | 15DF4                           | Jumper                              | Jumper                          |
| R280             | 2.0K                             | 2.0K                                 | 1.8K                            | 1.8K                                | 2.4K                            |
| L302 Linear Coil | 10uH                             | 10uH                                 | 10uH                            | 10uH                                | 6uH                             |
| C314             | 0.0039uF/1.6 KV                  | 0.0039uF/1.6 KV                      | 0.0039uF/1.6 KV                 | 0.0039uF/1.6 KV                     | 0.0047uF/1.6 KV                 |
| R328             | 39K                              | 12K                                  | 47K                             | 22K                                 |                                 |

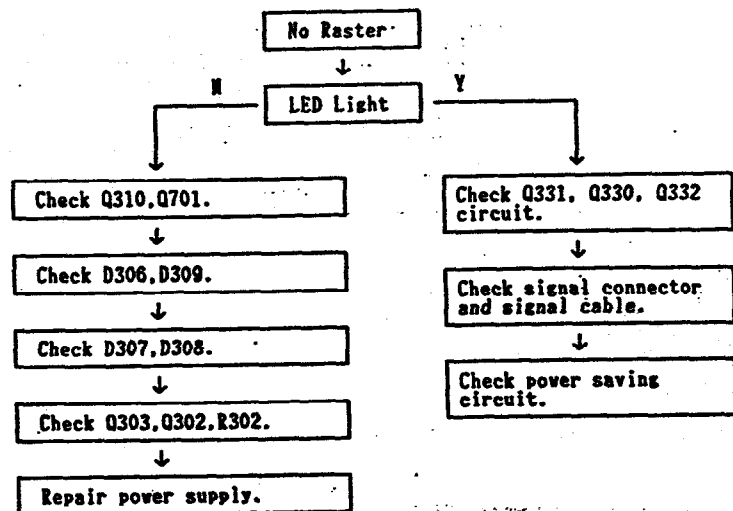
2. Main Board

| Location | LR CRT                                  | Normal CRT |
|----------|-----------------------------------------|------------|
| TP5      | W.A. 1P BLK 1015 #18 400 (50.75401.011) | Omit       |
| TP6      | W.A. 1P BLK 1618 #24 500 (50.75401.001) | Omit       |

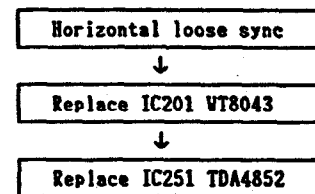
3. DC Level

|                     |              |              |
|---------------------|--------------|--------------|
| Coil Degaussing     | 19.90008.001 | 19.90015.001 |
| Clip CAB A1100 7056 | 34.75605.001 | Omit         |

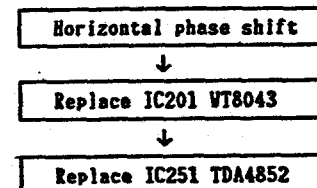
### 3.13.2 No Raster



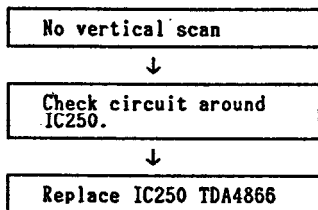
### 3.13.3 Horizontal Loose Sync



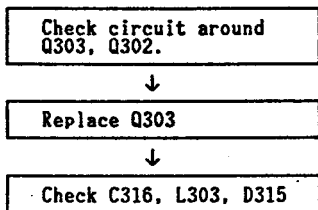
### 3.13.4 Horizontal Phase Shift

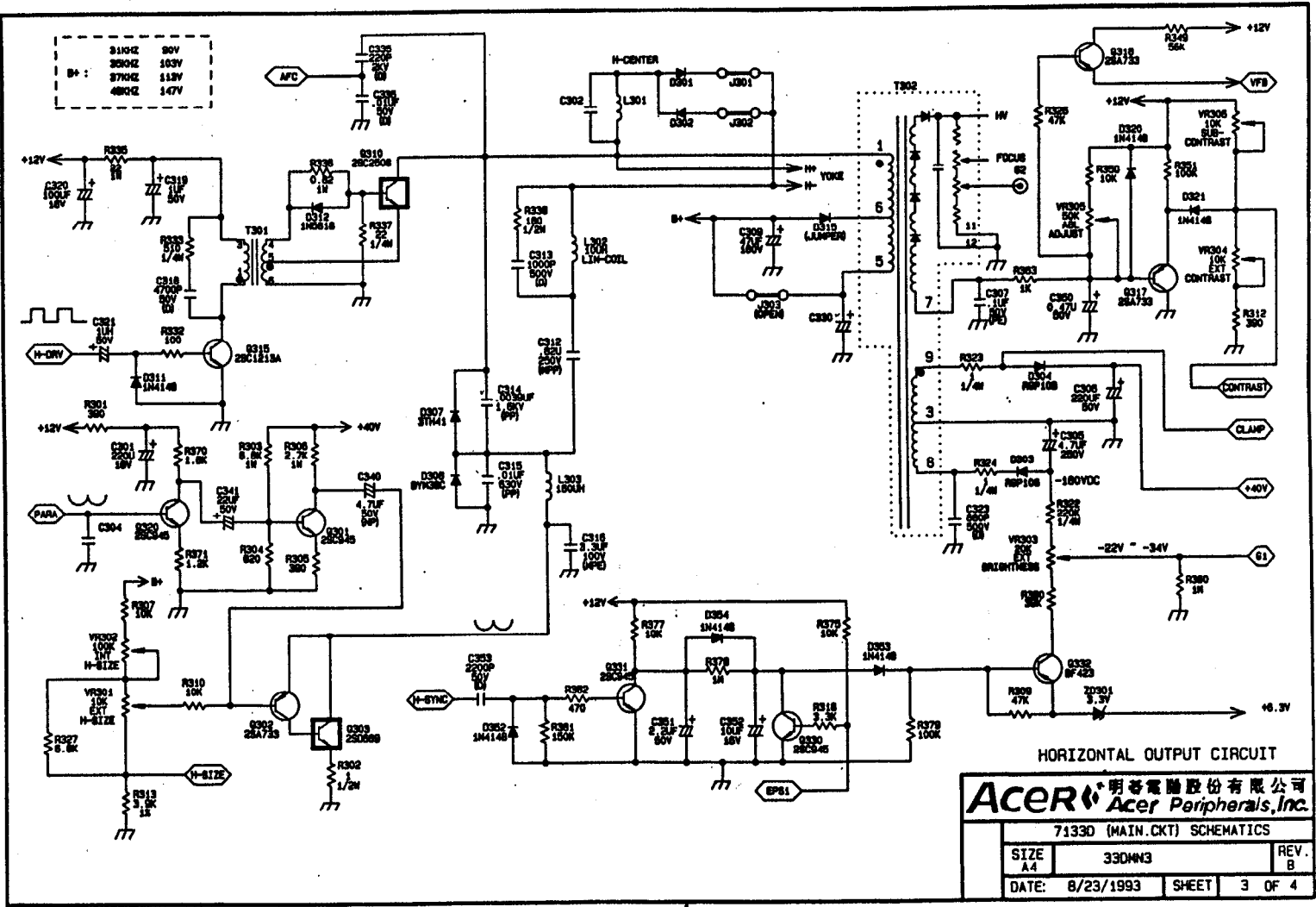


### 3.13.5 No Vertical Scan

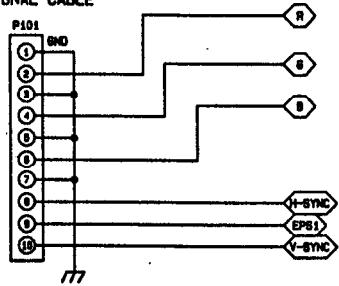


### 3.13.6 Horizontal Size and Pincushion Poor

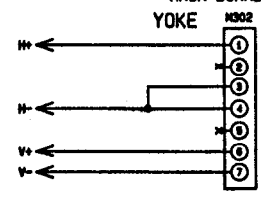




SIGNAL CABLE

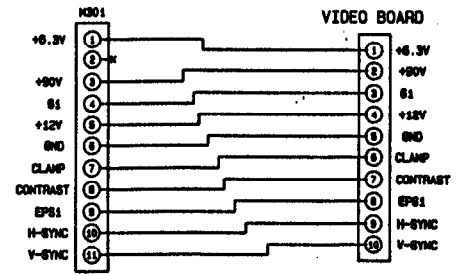


MAIN BOARD



MAIN BOARD

VIDEO BOARD

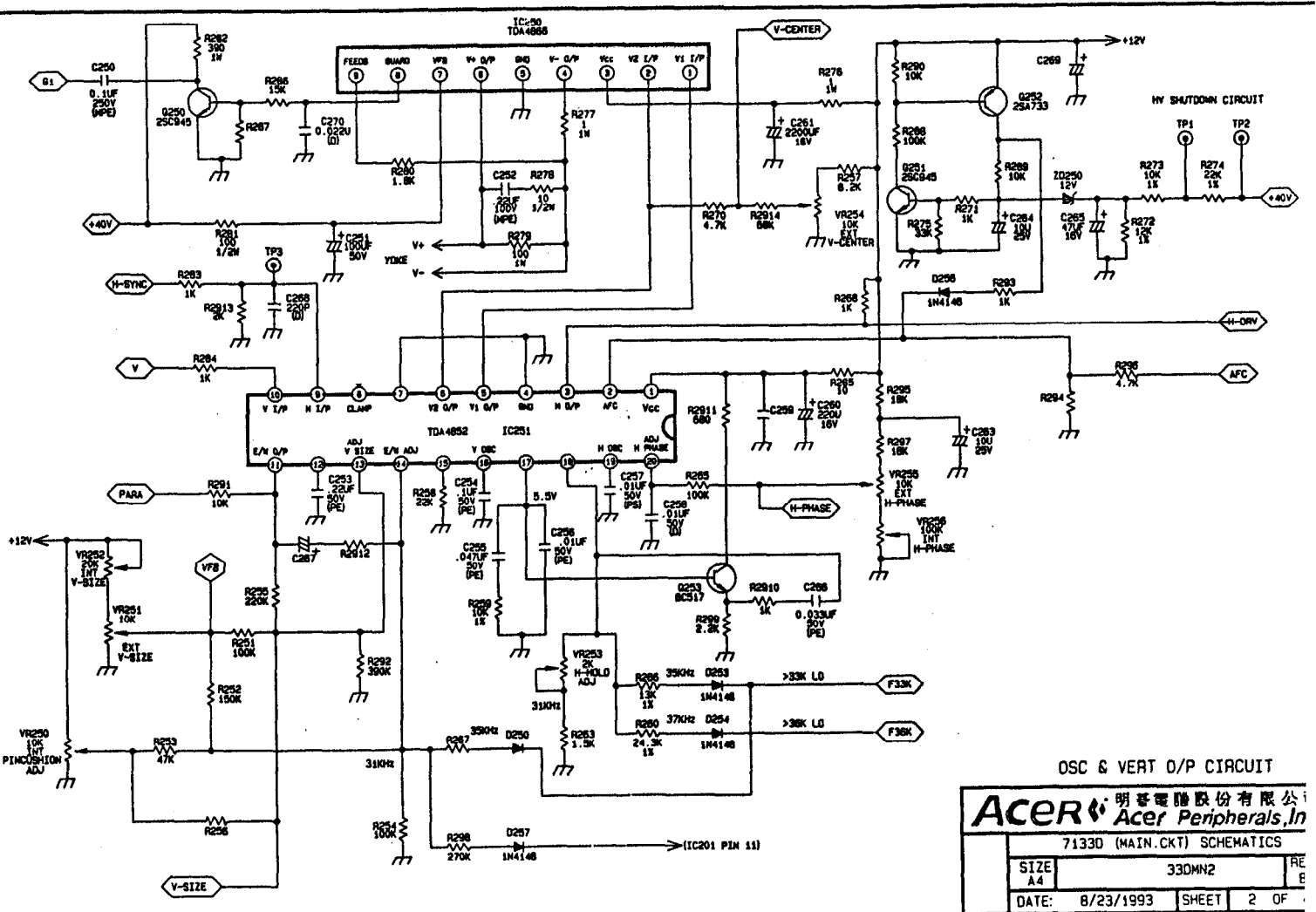
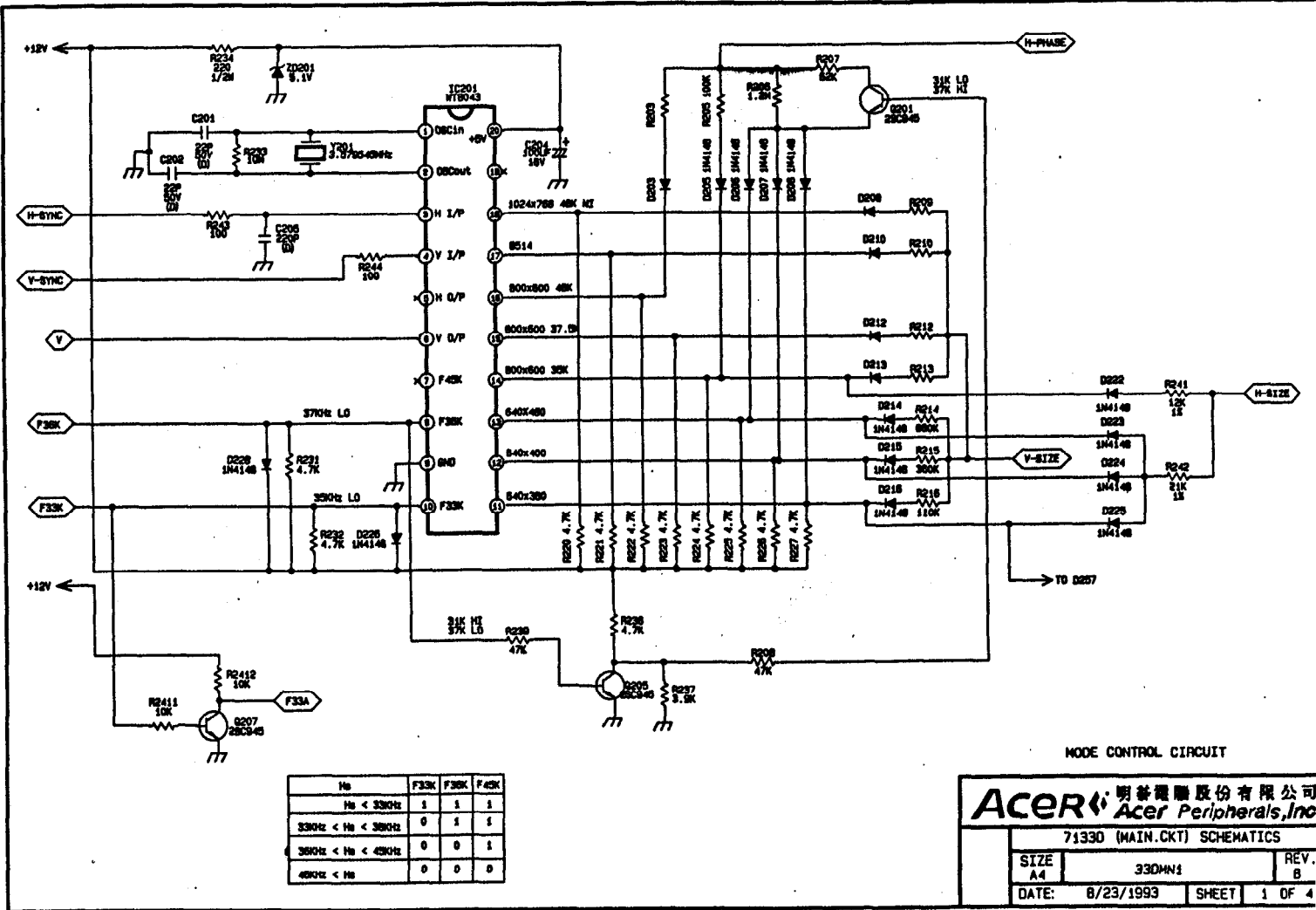


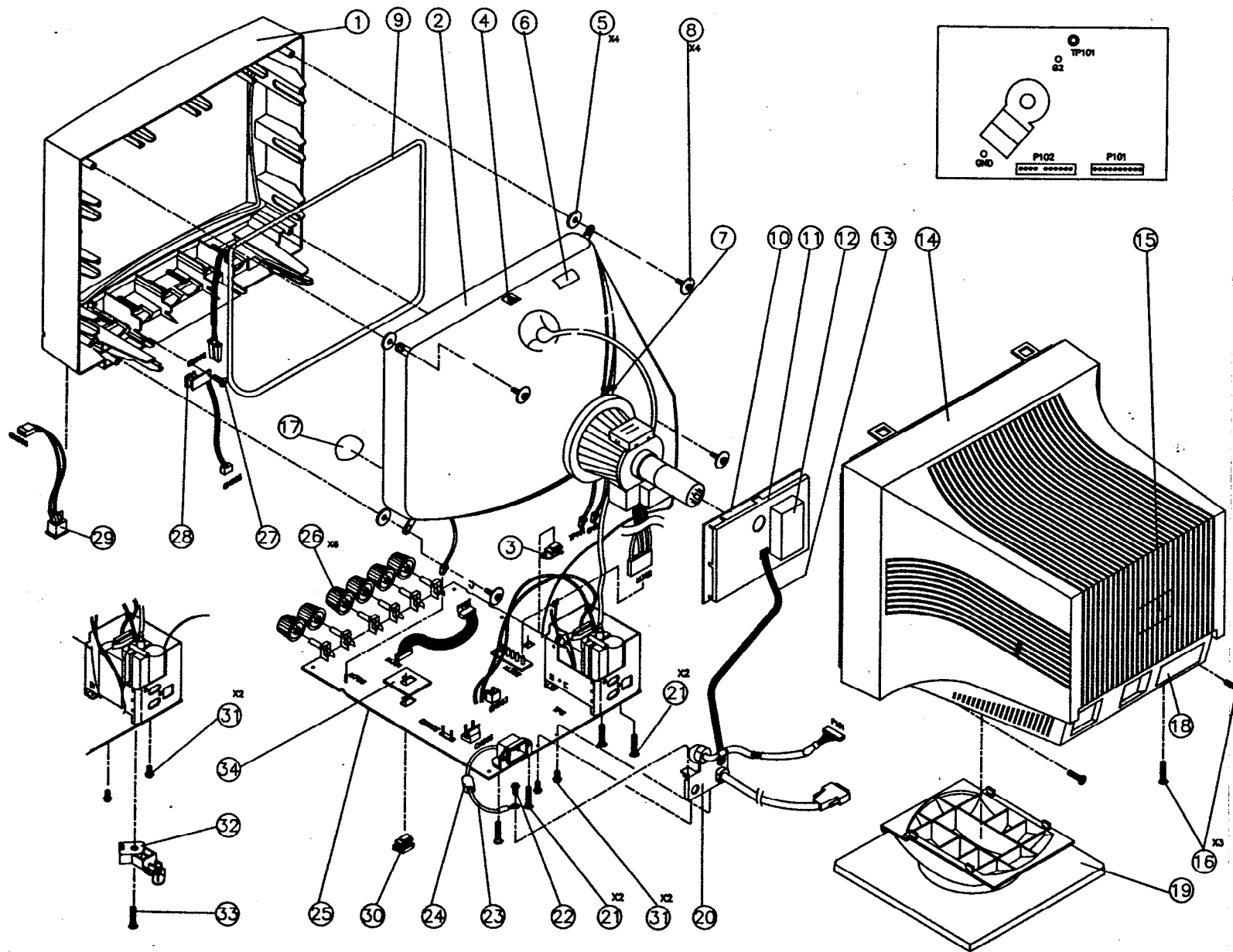
CONNECTOR CIRCUIT

**Acer** 明基電腦股份有限公司  
Acer Peripherals, Inc.

71330 (MAIN.CKT) SCHEMATICS

|       |           |        |      |      |
|-------|-----------|--------|------|------|
| SIZE  | A4        | 330MN4 | REV. | B    |
| DATE: | 8/23/1993 | SHEET  | 4    | OF 4 |





| ITEM | PART NO      | DESCRIPTION                    | QTY |
|------|--------------|--------------------------------|-----|
| 1    | 41.75301.000 | BEZEL ASSEMBLY                 | 1   |
|      | 41.75302.000 | BEZEL ORN                      | 1   |
|      | 41.75303.000 | BEZEL KEYPORT                  | 1   |
| 2    | 56.05753.000 | CRT 14" 71330                  | 1   |
|      | 56.05733.000 | CRT 14" 71347                  | 1   |
|      | 56.05711.000 | CRT 14" 71110                  | 1   |
| 3    | 42.75301.001 | SPACER FOR HYLOM 70330         | 1   |
| 4    | 45.71502.001 | LBL. MARK HV ART 7016          | 1   |
| 5    | 47.73101.001 | SPACER CRT BLK 7031            | 1   |
| 6    | 45.75301.001 | LBL. HV MARK ART 70330         | 4   |
| 7    | 50.75401.021 | WLA. 4P(CRT GND) BRAD 40284    | 1   |
| 8    | 88.0008.281  | SCREW TAP WASHER M5x2.5 0-24   | 4   |
| 9    | 19.90008.001 | COIL DEHUMIDIFYING             | 4   |
| 10   | 55.75402.001 | VIDEO BD 71347                 | 1   |
| 11   | 34.75801.001 | SHIELD CRT BD SPE 71330        | 1   |
| 12   | 47.71508.001 | SPONGE 80x80x30 7015           | 1   |
| 13   | 50.80311.011 | WLA. 1P(BRD) BRAD 11884        | 1   |
| 14   | 38.75301.000 | CASE U                         | 1   |
| 15   | 40.75301.000 | NAME PLATE                     | 1   |
| 16   | 88.0A528.180 | SCREW TAP CAP M4x1.6 18        | 3   |
| 17   | 40.75408.001 | EPA LBL FOR MONITOR            | 3   |
| 18   | 40.77801.001 | LBL. SPEED POLY                | 1   |
| 19   | 60.73101.000 | ASSEMBLY BASE ORN              | 1   |
| 20   | 60.75802.000 | ASSEMBLY REAR BKT              | 1   |
| 21   | 88.0A428.120 | SCREW TAP PAN M4x1.6 24-60     | 4   |
| 22   | 88.00010.181 | SCREW TAP W/EXT M4x1.6         | 1   |
| 23   | 50.81007.003 | WLA. 1P(BRD) 1015 (15 28034)   | 1   |
| 24   | 25.70044.001 | CORE D17.5x48.5x0.5            | 1   |
| 25   | 55.75401.000 | MAIN BOARD 71347               | 1   |
|      | 55.75801.000 | MAIN BOARD 71330               | 1   |
|      | 55.75801.000 | MAIN BOARD 71110               | 1   |
| 26   | 42.75302.000 | DOOR                           | 6   |
| 27   | 88.0A224.880 | SCREW TAP W/EXT M3x1.6 24      | 1   |
| 28   | 55.75803.001 | LED BD 71330                   | 1   |
| 29   | 60.75803.001 | ASSEMBLY SWITCH ROCK L/O 71330 | 1   |
| 30   | 42.75802.001 | SPACER TRANS HYLOM 71330       | 1   |
| 31   | 88.0C224.880 | SCREW TAP W/EXT M3x1.6 24      | 4   |
| 32   | 42.75801.001 | SPACER POST ASS 71330          | 1   |
| 33   | 88.0A224.120 | SCREW TAP W/EXT M3x1.6 24      | 1   |
| 34   | 45.75803.001 | PCB PER TRANS L 71330          | 1   |

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